

PTO/SB/08a/b (08-03)
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Substitute for form 1449A/B/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet	1	of	2		Complete if Known
				Application Number	10/555,344
				Filing Date	November 1, 2005
				First Named Inventor	Francois Costa, et al.
				Art Unit	Not Yet Assigned
				Examiner Name	Not Yet Assigned
				Attorney Docket Number	28944/50009

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
/DR/		EP 0 689 254-A1	12-27-1995	Motorola, Inc.	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language translation is attached.

NON PATENT LITERATURE DOCUMENTS					T ²
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			
/DR/		French Preliminary Search Report FR 0305601; report dated January 29, 2004.			
/DR/		International Search Report PCT/FR2004/001040; report dated November 4, 2004.			
/DR/		Written Opinion of the International Search Administration dated January, 2004.			
/DR/		"A New MOFSET & IGBT Gate Drive Insulated by a Piezoelectric Transformer", Vasic D et al., 2001 IEEE 32 nd Annual Power Electronics Specialists Conference, Vancouver, Canada, 17-21 June 2001, pages 1479-1484.			
		"Commande de Transistor a Grille Isolee Par Transformateurs Piezoelectriques: Bras Complet", Vasic and al, EPF 2002, Montpellier, November 2002.			
/DR/		"Piezoelectric Transformer Operating in Thickness Extensional Vibration and its Application to Switching Converter", Zaitzu and al, PESC 94. 1994			
		"Coreless printed circuit board (PCB) Transformer for Power MOFSET & IGBT gate drive circuit", HUI and al. IEEE, Trans. Power Electron., Vol. 14, pages 431-437, May 1999.			
		"Some Electromagnetic Aspects of Coreless PCB Transformers", HUI and al. IEEE, Trans. Power Electron., Vol. 14, pages 805-810, July 2000.			
		"Optimal Operation of coreless PCB Transformer Isolated Gate Drive Circuits with Wide Switching Frequency Range", HUI and al., IEEE transactions on Power Electronics, Vol. 14, pages 506-514, May 1999.			
		"Piezoelectric Transformer for Complementary MOFSET and IGBT Gate Drive Circuit", Vasic and al. EPE 2003, Toulouse, September 2003.			
		"New Design Method of Piezoelectric Transformer Considering High-Power Characteristics of Various Compositions Ceramics", Hirose and al. 1998, pages 953-959.			
/DR/		"Design of Piezo Transformers and Assemblies", Yerofeyev, Electrical Tehnology, N°4, 1996, pages 155-164.			

Examiner Signature	/Derek Rosenau/	Date Considered	04/12/2007
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Application Number	10/555,344
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Sheet	2	of	2	Attorney Docket Number	28944/50009

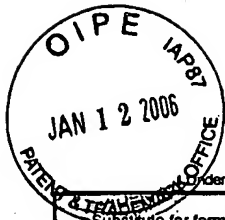
/DR/		"Design Considerations of Multi-Layer Piezoelectric Transformer", Prieto and al., 16 th Annual Applied Power Electronics Conference, Apec 2001, Anaheim, USA 4-8 March 2001, pages 1258-1263.	
/DR/		"A new Method to Design Piezoelectric Transformer used in MOSFET and IGBT Gate Drive Circuits", Vasic and al., 2003, IEEE 34 th Annual Power Electronics Specialist Conference, PESC 2003, Acapulco, Mexico, 15-19 June 2003, pages 307-312.	
/DR/		"Piezoelectric Technology Primer", James R. Phillips, Linear Technology, AN81-12 – AN81-21.	

No date/year information

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Substitute for form 1449A/B/PTO SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Complete if Known	
				Application Number	10/555,344
				Filing Date	November 1, 2005
				First Named Inventor	Francois Costa, et al.
				Art Unit	Not Yet Assigned
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/DR/		EP 0 581 580 A2	02-02-1994	SGS-Thomson Microelectronics, Inc.		

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